FIGURE 1

MASTER SYSTEM 100

PROCESSOR 101

MEMORY (INSTRUCTIONS, DATA) 102

STORAGE DEVICES (INSTRUCTIONS, DATA) 103

SYSTEM CONTROLLER 104

TO OTHER SLAVES, DEVICES, ETC. 143

109B

109A

TO OTHER SLAVES, DEVICES, ETC. 142

MEMORY, STORAGE DEVICES 131

CONTROLLER 134

TO OTHER SLAVES, DEVICES, ETC. 141

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SLAVE SYSTEM 110

PROGRAMMABLE INTERFACE** INCLUDING A SYSTEM CONTROLLER 114

PROCESSOR 111

MEMORY (INSTRUCTIONS, DATA) 112

STORAGE DEVICES (INSTRUCTIONS, DATA) 113

SLAVE SYSTEM 120

MEMORY (INSTRUCTIONS, DATA) 122

STORAGE DEVICES (INSTRUCTIONS, DATA) 123

PROGRAMMABLE INTERFACE** INCLUDING A SYSTEM CONTROLLER 124

PROCESSOR 121

** NOTES. IN ONE EMBODIMENT, THE PROGRAMMABLE INTERFACE IS CONFIGURED TO REDIRECT/TRANSLATE THE ORIGINAL BOOT ADDRESS TO A REMOTE BOOT ADDRESS FOR ACCESSING A REMOTE BOOT IMAGE. IN ONE EMBODIMENT, THE ORIGINAL BOOT ADDRESS REFERS TO A LOCATION IN THE BOOT ROM OF THE SLAVE SYSTEM FOR RETRIEVING A LOCALLY STORED BOOT IMAGE WITHIN THE FIRST SLAVE SYSTEM
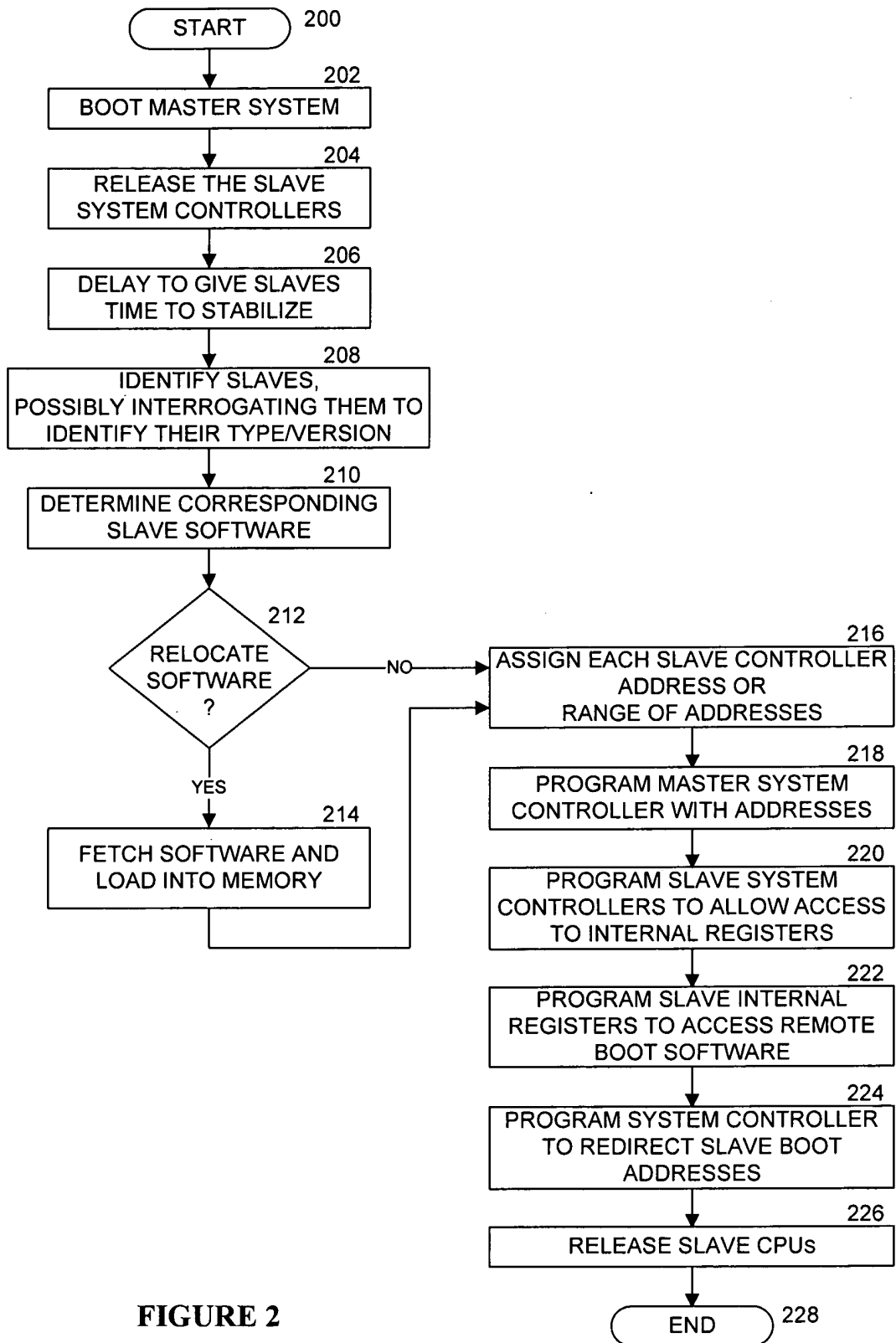
START `200`

↓

BOOT MASTER SYSTEM `202`

↓

RELEASE THE SLAVE
SYSTEM CONTROLLERS `204`

↓

DELAY TO GIVE SLAVES
TIME TO STABILIZE `206`

↓

IDENTIFY SLAVES,
POSSIBLY INTERROGATING THEM TO
IDENTIFY THEIR TYPE/VERSION `208`

↓

DETERMINE CORRESPONDING
SLAVE SOFTWARE `210`

↓

RELOCATE
SOFTWARE
? `212` —NO→ ASSIGN EACH SLAVE CONTROLLER
ADDRESS OR
RANGE OF ADDRESSES `216`

↓ YES

FETCH SOFTWARE AND
LOAD INTO MEMORY `214`

PROGRAM MASTER SYSTEM
CONTROLLER WITH ADDRESSES `218`

↓

PROGRAM SLAVE SYSTEM
CONTROLLERS TO ALLOW ACCESS
TO INTERNAL REGISTERS `220`

↓

PROGRAM SLAVE INTERNAL
REGISTERS TO ACCESS REMOTE
BOOT SOFTWARE `222`

↓

PROGRAM SYSTEM CONTROLLER
TO REDIRECT SLAVE BOOT
ADDRESSES `224`

↓

RELEASE SLAVE CPUs `226`

↓

END `228`

FIGURE 2